



MAZEN YOUSSEF
PHD Simulation and Modeling
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OBJECTIVE

Provide simple, universal access to information and services for all students. Research and provide more advanced tools for the Education to enhance its web presence in a way that will reach and attract more potential students and meet our community's changing needs. Improve the communications ability for individuals and groups associated with the Education. Expand and enhance technology support tools of Education's current needs and expectations. Maintain current technology hardware, software and network infrastructure so that it is reliably available.

EDUCATION AND QUALIFICATIONS:

Al-Baath university, Homs, Syria.

August 2009– present

Senior Lecturer in the department of electronic & telecommunication engineering, Faculty of Mechanical & Electronically Engineering

Paul Verlaine University – Metz, France.

2004–2009

PHD degree in Simulation & Modeling (63– Electronic, Optronics and systems – doctoral school Computer science, automation, electronics, electrical engineering, mathematics) with honor, Delivered on the 8th Jun 2009.

Paul Verlaine University – Metz, France.

2003–2004

Master 2 M&I (Microelectronics and Instrumentation) in digital system design

Tishreen University – Lattakia, Syria.

1998–1999

Diploma in telecommunications

Tishreen University – Lattakia, Syria.

1992–1997

Bachelor Degree in Electrical Engineering (Option: Electronics)



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WORK EXPERIENCE

Ministry of high education, **2023–present**

Al Baath University– Homs, Syria

- *Head of the Department of Electronics and Telecommunication Engineering*

Ministry of high education, **2009–present**

Al Baath University– Homs, Syria

Senior Lecturer in the department of electronics & telecommunication engineering, Faculty of Mechanical & Electronically Engineering:

- *Logic systems and digital circuit, Computer Operating Principles, Fundamentals of electronic engineering, Electronic circuit, Simulation & Modeling, Advanced digital system design, Design and analysis of digital and electronic circuits, Computer architecture, VLSI, Advanced VLSI, Communications, Telecommunication, Supervising the student's projects in last year's, Master and PHD. (For more Information see the table below)*

Ministry of high education, **2015–2018**

Al KALAMOON University– Damascus, Syria **& 2020– present**

Senior Lecturer in the department of Information Technology, Faculty of Engineering (Part-time):

- *Fundamental of digital electronics circuit, Advanced digital system, design using FPGA (VHDL), VLSI technology, Computer architecture, Simulation and modeling, Data transceiver, advanced communication, Information theory.*

Ministry of high education, **2017–2019**

Tartous University– Tartous, Syria

Senior Lecturer in the department of ICTE, Faculty of Engineering (Part-time):

- *Advanced digital system design, VLSI System, Computer architecture,*

Ministry of high education, **2018–2020**

Arab Private University for Science and

Technology, Hama, Syria

Senior Lecturer in the department of Information Technology IT, Faculty of Engineering (Part-time):

- *Computer architecture, Simulation and modeling, computer skills, Digital Communication*



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Research activity

- System on chip (SOCs) & Networks-on- Chip (NoCs)
- Study and Development the architectures used in 4G, 5G systems

SUPERVISING OF STAGES

Supervising many thesis for MASTER & PHD in Hardware

2014–present

Electronic & Digital device, *communication*. Al-Baath university,
 Homs–Syria

Subject: Network on Chip, Simulation & Modeling digital circuit , MIMO & Massive MIMO, MIMO–UWB, Graph theory, (using MATLAB & FPGA implementation)

Member of many jury for MASTER & PHD in

2014–present

electronics & telecommunication engineering

SKILLS

- Computer skills:
 - *Assembly, MATLAB, VHDL (Design with VHDL at the RTL level and implementation in FPGA).*
 - *Software: MATLAB , MATLAB Simulink, QUARTUS II.*
- Communication skills:
 - *UMTS (3G, W-CDMA), OFDM, MIMO, MU-MIMO, Massive MIMO, Wi-fi , WiMAX, UWB, Zigbee.*

LANGUAGES

	Reading	Speaking	Writing
French	Very good	Very good	Very good
English	Very good	good	good
Arabic	Mother tongue	Mother tongue	Mother Tongue



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PUBLICATIONS

- [1] M. Alkhadour, M. Al Ali, M. YOUSSEF “Enhancing BER Performance for MIMO–UWB System Using TR Technique” the 2th International Conference on Emerging Smart Technologies and Applications (eSmarTA), Year 2022.
- [2] M. Alkhadour, M. Al Ali, M. YOUSSEF “LOS Accurate Collaborative Localization based on Joint TDoA and AoA Measurements in UWB–MIMO Environment” the 5th International Conference of computer Science and Renewable Energies ICCSRE`2022.
- [3] R. Y. Shalp Alsham , R. A. Almostafa , Mazen Youssef : Low–latency Efficient Monitor–based Router for Asynchronous On–Chip Networks” 3rd International Conference on Electrical, Communication and Computer Engineering – ICECCE 2021.
- [4] M. YOUSSEF “Improving the estimation of the internal structure of the Graph, depending on the data set on graph nodes” Journal of AI – Baath University, 2020.
- [5] S. Al Ali, M. Youssf, J. Rizkallah, M. Hassan, A. Karfoul, “An efficient graph structure inference strategy based on random walk model on graph: Application to functional brain networks” Fifth International Conference on Advances in Biomedical Engineering (ICABME), Tripoli–Lebanon, 17–19 Oct. 2019
- [6] M. YOUSSEF , M. Al Ali “ Electronic Engineering 1” Book for the third year in the department of electronic & telecommunication engineering, Faculty of Mechanical & Electronically Engineering, 2019
- [7] M. YOUSSEF “The performance and efficacy of the design at RTL–FPGA level when increasing the number of antennas in the MIMO transmitter systems” Journal of AI – Baath University Volume 41, 2019
- [8] M. Alkhadour, M. YOUSSEF “The Usage of Time Reverse Technology to Improve the BER Performance and Transmission Rate of IR–UWB System” Al–Nahrain Journal for Engineering Sciences, Iraq ,2019
- [9] A. JEHA, M. YOUSSEF “Comparison of the performance of the linear beam forming techniques applied to the downlink of multi–user MIMO system” Journal of AI – Baath University Volume 41, 2019
- [10] S. Al–ALI, M. YOUSSEF, “Improved positioning algorithms based on RSSI using spatial diversity technology” Journal of AI – Baath University Volume 41, 2019
- [11] M.YOUSSEF, F.MONTEIRO, A.DANDACHE, C.DIOU “An Effective and Scalable Multiuser Architecture for the Base Station Receiver”, the 2nd International Conference on Signal Processing and Communication Systems, ICSPCS’2008, from 15–17 December 2008, Gold Coast, Australia”
- [12] M. YOUSSEF, F. MONTEIRO, A. DANDACHE, C. DIOU “A Cost–Effective Parallel Architecture for the CodeRAKE Receiver” Proc. 14th IEEE International Conference on Electronics, Circuits, and Systems, pp 447–450, Dec 2007.



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- [13] M. YOUSSEF, C. DIOU, F. MONTEIRO, A. DANDACHE “CodeRAKE: a new small-area scalable architecture for the multi-user/multi-code RAKE receiver” Proc. 13th IEEE International Conference on Electronics, Circuits, and Systems, pp 148–151, Dec 2006.
- [14] M. YOUSSEF, F. MONTEIRO, A. DANDACHE, C. DIOU, « CodeRAKE: a scalable and cost-effective RAKE architecture for multi-user reception », GDR System on Chip – System in Package, 13–15 june 2007, Paris, France.
- [15] M.YOUSSEF, “RAKE receiver architecture”, 1st Winter School, Self Organization in Embedded Systems SOES’06, Schloss Dagstuhl, Germany, November 2006.



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Ministry of high education, Al Baath University– Homs, Syria

Senior Lecture	Department	Credit Hours	Semester	Year
Computer Operating Principles	First year Control and Computers	6	First	2010 – 2009
	First year Electrical Engineering	6		
Simulation & Modeling	Fourth year Mechatronic Engineering	4		
Fundamentals of electronic engineering	Second year Mechatronic Engineering	6	second	
Logic systems and digital circuit	Third year Control and Computers	6		
Bachelor project	Fifth year communications and electronic engineering	6		
electronic /1/	Third year Control and Computers	6	First	2011 – 2010
	Third year communications and electronic engineering	6		
Computer Operating Principles	First year Control and Computers	6		
	First year Electrical Engineering	6		
Simulation & Modeling	Fourth year Mechatronic Engineering	4		
Fundamentals of electronic engineering	Second year Mechatronic Engineering	6	second	
Logic systems and digital circuit	Third year Control and Computers	6		



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electronic engineering/2/	Third year Control and Computers	6				
Bachelor project	Fifth year communications and electronic engineering	6				
electronic /1/	Third year Control and Computers	6	First	2012 – 2011		
	Third year communications and electronic engineering	6				
Computer Architecture	Third year Mechatronic Engineering	6				
Simulation & Modeling	Fourth year Mechatronic Engineering	4				
Fundamentals of electronic engineering	Second year Mechatronic Engineering	6	Second			
Logic systems and digital circuit	Third year Control and Computers	6				
electronic /2/	Third year Control and Computers	6				
Bachelor project	Fifth year communications and electronic engineering	6				
electronic /1/	Third year Control and Computers	6	First	2013 – 2012		
	Third year communications and electronic engineering	6				
Computer Architecture	Third year Mechatronic Engineering	6				
Telecommunication	Fourth year Control and Computers	6				



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Fundamentals of electronic engineering	Second year Mechatronic Engineering	6	Second		
Logic systems and digital circuit	Third year Control and Computers	6			
electronic /2/	Third year Control and Computers	6			
Bachelor project	Fifth year communications and electronic engineering	6			
electronic /1/	Third year Control and Computers	6	First	2014 – 2013	
	Third year communications and electronic engineering	6			
Computer Architecture	Third year Mechatronic Engineering	6			
Telecommunication	Fourth year Control and Computers	6			
Communication	Fifth year Control and Computers	4	Second		
Logic systems and digital circuit	Third year Control and Computers	6			
electronic /2/	Third year Control and Computers	6			
Bachelor project	Fifth year communications and electronic engineering	6			
electronic /1/	Third year Control and Computers	6	First	2015 – 2014	
	Third year communications and electronic engineering	6			
Computer Architecture	Third year	6			



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	Mechatronic Engineering			
Telecommunication	Fourth year Control and Computers	6		
Communication	Fifth year Control and Computers	4		
Logic systems and digital circuit	Third year Control and Computers	6		
Computer Architecture	Fourth year Control and Computers	6	Second	
Bachelor project	Fifth year communications and electronic engineering	6		
electronic /1/	Third year Control and Computers	6		
	Third year communications and electronic engineering	6	First	
Computer Architecture	Third year Mechatronic Engineering	6		
Telecommunication	Fourth year Control and Computers	6		
Communication	Fifth year Control and Computers	4		
Logic systems and digital circuit	Third year Control and Computers	6		
electronic /2/	Third year Control and Computers	6		
Computer Architecture	Fourth year Control and Computers	6	Second	
Bachelor project	Fifth year communications and electronic engineering	6		
electronic /1/	Third year	6	First	2016 – 2015



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	Control and Computers			
Integrated Circuits	Fourth year communications and electronic engineering	6		
Simulation & Modeling	Fourth year Mechatronic Engineering	4		
Testability (digital Circuit)	Master degree in Electronic	4	Second	
Fundamentals of electronic engineering	Second year Mechatronic Engineering	6		
Logic systems and digital circuit	Third year Control and Computers	6		
Computer Architecture	Fourth year Control and Computers	6		
Testability (digital Circuit)	Master degree in Electronic	4		
Bachelor project	Fifth year communications and electronic engineering	6		
electronic /1/	Third year Control and Computers	6	First	2018 – 2017
	Third year communications and electronic engineering	6		
Integrated Circuits	Fourth year communications and electronic engineering	6		
Simulation & Modeling	Fourth year Mechatronic Engineering	4		
Testability (digital Circuit)	Master degree in Electronic	4		
Fundamentals of electronic engineering	Second year Mechatronic Engineering	6	Second	
Logic systems and digital circuit	Third year Control and Computers	6		
Testability (digital Circuit)	Master degree in Electronic	4		



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Bachelor project	Fifth year communications and electronic engineering	6		
electronic /1/	Third year Control and Computers	6	First	2019 – 2018
	Third year communications and electronic engineering	6		
Integrated Circuits	Fourth year communications and electronic engineering	6		
Simulation & Modeling	Fourth year Mechatronic Engineering	4		
Testability (digital Circuit)	Master degree in Electronic	4		
Fundamentals of electronic engineering	Second year Mechatronic Engineering	6	Second	
Logic systems and digital circuit	Third year Control and Computers	6		
Testability (digital Circuit)	Master degree in Electronic	4		
Bachelor project	Fifth year communications and electronic engineering	6		
electronic /1/	Third year Control and Computers	6	First	2020 – 2019 & 2020–2021 Present
	Third year communications and electronic engineering	6		
Simulation & Modeling	Fourth year Mechatronic Engineering	4		
Testability (digital Circuit)	Master degree in Electronic	4		
Fundamentals of electronic engineering	Second year Mechatronic Engineering	6	Second	



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Logic systems and digital circuit	Third year Control and Computers	6		
Testability digital Circuit	First year Electronic Master	4		

Ministry of high education, Al KALAMOON University– Damascus, Syria

Senior lecturer	Department	Credit Hours	Semester	Year
Fundamental of digital electronics circuit	Second year Information technology	3	First	2020-2021
	Second year Mechatronic Engineering			
Organization and Computer Architecture	Third year Information technology	3		
Microprocessor systems	Third year Information technology	3		
Very Large scale Integrated Circuit VLSI	Fourth year Communication	3		
Advanced Digital circuit	Fifth year Communication			
Project	Fourth year Information technology	2		
Bachelor project	Fifth year Information technology	3		
Fundamental of digital electronics circuit	Second year Information technology	3	second	
	Second year Mechatronic Engineering	3		
Information Theory	Third year Information technology	3		



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	Third year Communication	3		
Project	Fourth year Information technology	2		
Bachelor project	Fifth year Information technology	3		
Fundamental of digital electronics circuit	Second year Information technology	3	Third	
	Second year Mechatronic Engineering	3		
Fundamental of digital electronics circuit	Second year Information technology	3	First	2021–2022
	Second year Mechatronic Engineering	3		
Information Theory	Third year Information technology	3		
	Third year Communication	3		
Project	Fourth year Information technology	2		
Bachelor project	Fifth year Information technology	3		
Fundamental of digital electronics circuit	Second year Information technology	3	Second	
	Second year Mechatronic Engineering	3		
Logic systems and digital circuit	Third year Control and Computers	6		
Project	Fourth year Information technology	2		
Bachelor project	Fifth year	3		



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	Information technology			
Fundamental of digital electronics circuit	Second year Information technology	3	Third	
	Second year Mechatronic Engineering	3		
Simulation and Modeling System	Fourth year Information technology	3		