

MODULE DESCRIPTION FORM

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	Operating Systems		Module Delivery	
Module Type	Core		☒ Theory ☐ Lecture ☒ Lab ☐ Tutorial ☐ Practical ☐ Seminar	
Module Code	3101CET			
ECTS Credits	5			
SWL (hr/sem)	125			
Module Level	3	Semester of Delivery		5
Administering Department	CET	College	ETC	
Module Leader	Nadwa Sabeeh Jaber		e-mail	Nadwa.Sabeeh@alayer.edu.iq
Module Leader's Acad. Title	assistant lecrurer		Module Leader's Qualification	Master's
Module Tutor	Zain Alabdeen Abbas		e-mail	alabdeen568@gmail.com
Peer Reviewer Name	Asst. Prof. Ghadir Ibrahim Madi		e-mail	Ghadir.Ibrahim@alayer.edu.iq
Scientific Committee Approval Date	29/09/2025	Version Number	1.0	

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents

أهداف المادة الدراسية ونتائج التعلم والمحتويات الأساسية

Module Aims أهداف المادة الدراسية	1. This course includes the basic concepts of operating system components. 2. To develop problem-solving skills and understand process management, deadlocks, and synchronization. 3. To understand consists of memory management techniques. 4. This course deals with File system implementation. 5. It also includes a case study on the Linux operating system. 6. To understand the I/O device management principles. 7. To perform the disk Structure, Disk Scheduling (FCFS, SSTF, SCAN, CSCAN, LOOK, CLOOK), and Disk Formatting.
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	1. Should understand: hardware components that must be managed by an operating system. 2. Describe need and role of operating system. 3. The concept of a process, the process life cycle, process states and state transitions, process control blocks (PCBs)/process descriptors. 4. How processors transition between processes via context switching. How interrupts enable hardware to communicate with software. How processes converse with one another via interprocess communication (IPC). 5. The motivation for creating threads. The similarities and differences between processes and threads. The various levels of support for threads. The life cycle of a thread. Thread signaling and cancellation. 6. The challenges of synchronizing concurrent processes and threads. Critical sections and the need for mutual exclusion. how to implement mutual exclusion primitives in software 7. How monitors synchronize access to data. How condition variables are used with monitors. Solutions for classic problems in concurrent programming such as readers and writers and circular buffer. 8. The problem of deadlock. The four necessary conditions for deadlock to exist. The problem of indefinite postponement. The notions of deadlock prevention, avoidance, detection and recovery. 9. Understand OS components such a scheduler, memory manager, file 10. System handlers and I/O device managers. 11. Analyze and criticize techniques used in OS components 12. Demonstrate and simulate algorithms used in OS components 13. Identify algorithms and techniques used in different components of Linux

Indicative Contents المحتويات الإرشادية	1. Operating System Overview teaching hours: 10 hrs 2. Process Management teaching hours: 10 hrs 3. Process Deadlocks teaching hours: 10 hrs 4. Memory Management teaching hours: 14 hrs 5. File Management teaching hours: 10 hrs 6. Device Management teaching hours: 10 hrs 7. Linux Case Study teaching hours: 10 hrs

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering types of simple experiments involving some sampling activities that are interesting to An operating system that acts as an intermediary between the user of a computer and the computer hardware. The purpose of an operating system is to provide an environment in which a user can execute programs in a convenient and efficient manner.
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Student Workload (SWL)

الحمل الدراسي للطالب موزع على (15) اسبوع

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	64	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4.26
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	61	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	4.06
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال	125		

الفصل	
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Module Evaluation					
تقييم المادة الدراسية					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	5, 10	LO #1-4, LO #5-9
	Assignments	2	20% (10)	2, 12	LO #1,2, LO #3-10
	Report	1	10% (10)	continuous	
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-8
	Final Exam	4hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
المنهاج الاسبوعي النظري	
	Material Covered
Week 1	Introduction to Operating Systems Operating System Architectures, Definition, Two views of operating system, Evolution of operating system, Types of OS
Week 2	System Call, Handling System Calls, System Programs, Operating System Structures, The Shell, Open Source Operating Systems
Week 3	Process vs Program, Multiprogramming, Process Model, Process States, Process Control Block. Threads, Thread vs Process, User and Kernel Space Threads. Inter Process Communication, Race Condition, Critical Section
Week 4	Implementing Mutual Exclusion: Mutual Exclusion with Busy Waiting (Disabling Interrupts, Lock Variables, Strict Alteration, Peterson's Solution, Test and Set Lock), Sleep and Wakeup, Semaphore, Monitors, Message Passing, Classical IPC problems: Producer Consumer, Sleeping Barber, Dining Philosopher Problem.
Week 5	Process Scheduling: Goals, Batch System Scheduling (First-Come First-Served, Shortest Job First, Shortest Remaining Time Next), Interactive System Scheduling (Round-Robin Scheduling, Priority Scheduling, Multiple Queues), Overview of Real Time System Scheduling.
Week 6	Introduction, Deadlock Characterization, Preemptable and Non-preemptable Resources, Resource - Allocation Graph, Conditions for Deadlock.
Week 7	Midterm Exam

Week 8	Handling Deadlocks: Ostrich Algorithm, Deadlock prevention, Deadlock Avoidance, Deadlock Detection (For Single and Multiple Resource Instances), Recovery From Deadlock (Through Preemption and Rollback. Introduction, Monoprogramming vs. Multi-programming, Modelling Multiprogramming, Multiprogramming with fixed and variable partitions, Relocation and Protection. Memory management (Bitmaps & Linked-list), Memory Allocation Strategies.
Week 9	Virtual memory: Paging, Page Table, Page Table Structure, Handling Page Faults, TLB's Page Replacement Algorithms: FIFO, Second Chance, LRU, Optimal, LFU, Clock, WS- Clock,
Week 10	Concept of Segmentation: Need of Segmentation, its Drawbacks, Segmentation with Paging(MULTICS).
Week 11	File Overview: File Naming, File Structure, File Types, File Access, File Attributes, File Operations, Single Level, two Level and Hierarchical Directory Systems, File System Layout.
Week 12	Implementing Files: Contiguous allocation, Linked List Allocation, Linked List Allocation using Table in Memory, Inodes. Directory Operations, Path Names, Directory Implementation, Shared Files
Week 13	Free Space Management: Bitmaps, Linked List
Week 14	Classification of IO devices, Controllers, Memory Mapped IO, DMA Operation, Interrupts, Goals of IO Software, Handling IO(Programmed IO, Interrupt Driven IO, IO using DMA), IO Software Layers (Interrupt Handlers, Device Drivers) . Disk Structure, Disk Scheduling (FCFS, SSTF, SCAN, CSCAN, LOOK, CLOOK), Disk Formatting (Cylinder Skew, Interleaving, Error handling), RAID.
Week 15	History, Kernel Modules, Process Management, Scheduling, Inter-process Communication, Memory Management, File System Management Approaches, Device Management Approaches.

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

	Material Covered
Week 1	Lab 1: Introduction to Demonstration of basic Linux Commands
Week 2	Lab 2: Process creation and termination, thread creation and termination
Week 3	Lab 3: Simulation of IPC techniques
Week 4	Lab 4: Simulation process Scheduling algorithms
Week 5	Lab 5: Simulation of page replacement algorithms
Week 6	Lab 6: Simulation of File allocation techniques
Week 7	Lab 7: Simulate free space management techniques

Week 8	Lab 8: Simulation of disk scheduling algorithms
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Learning and Teaching Resources		
مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	Operating Systems (3rd Edition) 3rd Edition by Harvey M. Deitel (Author), Paul J. Deitel (Author), David R. Choffnes (Author)	Yes
Recommended Texts	Operating System Concepts Essentials Tenth Edition Avi Silberschatz Peter Baer Galvin Greg Gagne	yes
Websites		

Grading Scheme				
مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX - Fail	راسب (قيد الامتحان)	(45-49)	More work required but credit awarded
	F - Fail	راسب	(0-44)	Considerable amount of work required

Note: Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

MODULE DESCRIPTION FORM

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	Control Engineering Fundamentals		Module Delivery	
Module Type	Core		☒ Theory ☐ Lecture ☒ Lab ☐ Tutorial ☐ Practical ☐ Seminar	
Module Code	3102CET			
ECTS Credits	5			
SWL (hr/sem)	125			
Module Level	3	Semester of Delivery		5
Administering Department	CET	College	EETC	
Module Leader	Zahra Abbas Hassan Taiah		e-mail	Zahra.Abbas@alayen.edu.iq
Module Leader's Acad. Title	Lecturer	Module Leader's Qualification	PHD	
Module Tutor	Dr. Mazen Youssef		e-mail	Mazen.mohammad@alayen.edu.iq
Peer Reviewer Name	Asst. Prof. Ghadir Ibrahim Madi		e-mail	Ghadir.Ibrahim@alayen.edu.iq
Scientific Committee Approval Date	29/09/2025	Version Number	1.0	

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents

أهداف المهادة الدراسية ونتائج التعلم والمحتويات الإرشادية

Module Aims أهداف المهادة الدراسية	1. To define the control systems. 2. To develop mathematical models that accurately represent the behavior of the system 3. To simplify the representation of a control system. 4. To examine the system's behavior during the transient period and the steady state. 5. To design controllers that can manipulate the system or process to achieve desired objectives.
Module Learning Outcomes مخرجات التعلم للمهاده الدراسية	1. Define the control system. 2. classify the different types of control systems. 3. Describe a physical system in terms of differential equations 4. Use Laplace Transform in solving differential equations of the Control System. 5. Derive Transfer Function for describing the work of servomotors. 6. Reduce a block diagram of multiple subsystems to a single block representing the Transfer Function of the system. 7. Understand steady state and transient time response analysis. 8. Find error Coefficients and steady-state error (e_{ss}) according to system type. 9. Find the time response of the 1st order system. 10. Find the time response of the 2nd order system. 11. Understand the effect of damping ratio ζ on 2nd order system. 12. Identify Transient response specifications. 13. Define PID controllers. 14. Reduce the effect of Steady-state error (e_{ss}) and settling time (T_s) on time response using PID controller.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: <u>Part A – Basics of Control Systems and Transfer Function</u> Control System definitions, Classification of Control Systems, Comparison of Open Loop and Closed Loop Control Systems, Use Laplace Transform in Control System, Mathematical Modelling of Control Systems: Electrical Systems and Mechanical Systems (Translational and Rotational), Servomotors, Rules of Block diagram reduction. [24 hrs]

	<u>Part B – Time Response Analysis of Control Systems</u> Definitions: time response, transient response and steady state response, standard test inputs, steady state analysis, static error coefficient method, analysis of type 0,1 and 2 systems, transient response analysis: 1st order and 2nd order systems. [30 hrs] PID controllers: PD controller, PI controller, PID controller and output derivative controller [20 hrs]
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Learning and Teaching Strategies استراتيجيات التعلم والتعليم	
Strategies	The main strategy that will be adopted in delivering this module focuses on fostering active student engagement during exercises, fostering the development of critical thinking skills, and encouraging participation. This will be accomplished through a combination of classroom instruction, interactive tutorials, and the inclusion of engaging experiments that involve sampling activities that capture students' interest. The aim is to refine and enhance students' critical thinking abilities while ensuring their active involvement in the learning process.

Student Workload (SWL) الحمل الدراسي للطالب موزع على (15) اسبوع			
Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	64	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4.26
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	61	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	4.06
Total SWL (h/sem)	125		

الحيل الدراسي الكلي للطالب خلال الفصل	
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Module Evaluation					
تقييم المادة الدراسية					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	6, 10	LO #1-5, LO #6-9
	Assignments	2	10% (10)	8, 13	LO #1-7, LO #7-10
	Projects / Lab.	1	10% (10)	Continuous	
	Report	1	10% (10)	14	LO #1-13
Summative assessment	Midterm Exam	2 hr	10% (10)	8	LO # 1-7
	Final Exam	4hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
المنهاج الاسبوعي النظري	
	Material Covered
Week 1	Introduction – Basics of Control Systems
Week 2	Use of Laplace Transform in Control System
Week 3	Mathematical Modelling of Control System: Electrical System
Week 4	Mathematical Modelling of Control System: Translational Mechanical System
Week 5	Mathematical Modelling of Control System: Rotational Mechanical System
Week 6	Servomotors
Week 7	Block Diagram Reduction
Week 8	Mid-term Exam
Week 9	Time Response Analysis of Control Systems
Week 10	Analysis of Type 0, 1, and 2 systems
Week 11	Transient Response Analysis
Week 12	Analysis of 2 nd order system
Week 13	Transient response specifications
Week 14	PID controllers

Week 15	Rate feedback controller
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Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

	Material Covered
Week 1	Lab 1: Introduction to MATLAB Simulink
Week 2	Lab 2: Laplace Transform / Verifying Algebraic functions
Week 3	Lab 3: Laplace Transform / Verifying Sine functions
Week 4	Lab 4: Block Diagram Reduction
Week 5	Lab 5: Steady State Error
Week 6	Lab 6: 1 st Order System
Week 7	Lab 7: 2 nd Order System
Week 8	Lab 8: Proportional Controller/ P Controller Used in Closed-Loop DC Servo Motor Speed Control System
Week 9	Lab 9: Proportional Controller/ P Controller Used in Closed-Loop DC Servo Motor Position Control System
Week 10	Lab 10: Integral Controller/ I Controller Used in Closed-Loop DC Servo Motor Speed Control System
Week 11	Lab 11: Integral Controller/ I Controller Used in Closed-Loop DC Servo Motor Position Control System
Week 12	Lab 12: Derivative Controller/ D Controller Used in Closed-Loop DC Servo Motor Speed Control System
Week 13	Lab 13: Derivative Controller/ D Controller Used in Closed-Loop DC Servo Motor Position Control System
Week 14 & 15	Lab 14: PID Controller

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	Modern Control Engineering, K. Ogata, 2010 Pearson Education	Yes
Recommended Texts	1. Control Systems Engineering, U.A. Bakshi and S.C. Goyal, 2007 Technical Publications.	No

	2. Modern Control Systems, R. Dorf and R. Bishop, 2011 Pearson Education	
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Grading Scheme مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX - Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F - Fail	راسب	(0-44)	Considerable amount of work required
Note: Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

MODULE DESCRIPTION FORM

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	Digital Signal Processing		Module Delivery	
Module Type	Core		☒ Theory ☐ Lecture ☒ Lab ☐ Tutorial ☐ Practical ☐ Seminar	
Module Code	3103CET			
ECTS Credits	5			
SWL (hr/sem)	125			
Module Level	3	Semester of Delivery		5
Administering Department	CET	College	ETC	
Module Leader	Dr. Yousif Ward Sakr		e-mail	ywsfward7@gmail.com
Module Leader's Acad. Title	Lecturer	Module Leader's Qualification	Ph.D.	
Module Tutor	Dr. Bashar Hindawi Hewaidi		e-mail	bashar.hindawi@alayer.edu.iq
Peer Reviewer Name	Asst. Prof. Ghadir Ibrahim Madi		e-mail	Ghadir.Ibrahim@alayer.edu.iq
Scientific Committee Approval Date	01/10/2025	Version Number	1.0	

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents

أهداف الهادة الدراسية ونتائج التعلم والمحتويات الإرشادية

Module Aims أهداف الهادة الدراسية	1. Demonstrate an understanding of basic discrete-time systems, linearity, time-invariance, stability, impulse response and discrete convolution. 2. Implement discrete time systems, recursive and nonrecursive realizations. 3. Perform Z transform and finding the inverse Z transform including its properties. 4. Demonstrate an understanding of frequency analysis of both continuous and discrete signals. 5. Demonstrate an understanding of frequency response of linear time invariant systems. 6. Demonstrate an understanding of discrete Fourier transform, its properties and applications. 7. Design FIR and IIR digital filters.
Module Learning Outcomes مخرجات التعلم للهادة الدراسية	1. State, prove and apply Shannon's sampling theorem 2. Relate signal to noise ratio (SNR) to number of samples averaged in signal sampling and averaging systems 3. Implement sampling of continuous time signals and reconstruct them from their samples by choosing appropriate parameters and functions. 4. Change the sampling rate of discrete-time signals, avoiding folding effects. 5. Describe the fundamental properties of linear time invariant systems. 6. Analyze signals and systems in the discrete time domain. 7. Compute the frequency response of linear and time-invariant discrete-time systems, implement decomposition into a minimum-phase system and an all-pass system, and describe generalized linear-phase systems. 8. Implement discrete-time systems using various structures. 9. Understand the importance of the discrete Fourier transform and algorithms for its fast computation. 10. Analyze discrete-time signals in the frequency domain, using the windowing method as well as the time-dependent discrete Fourier transform, and reconstruct the signal with the overlap-sum algorithm. 11. Write down, state the properties of, and apply Fourier Transforms in DSP systems 12. Analyze and implement systems in the field of Z transformation. 13. Design basic finite impulse response (FIR) and infinite impulse response (IIR)

	filters.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following. <u>Introduction to DSP</u> Introduction to DSP, discrete signals and their properties. In addition, the concept of frequency in continuous time and discrete time signals. [5 hrs] <u>Discrete systems</u> Discrete systems, linear time-invariant systems, convolution theorem; Digital Signal Processing (DSP) is concerned with the processing of signals that are represented as sequences of finite-precision numbers. [10 hrs] <u>Sampling and reconstruction of analogue signals</u> Review of continuous-time signal and system analysis using Fourier ; Ideal impulse sampling and reconstruction of bandlimited signals; digital to analogue conversion, and practical considerations. [10 hrs] <u>Discrete-time sequences</u> Discrete-time signals and systems, linearity, time-invariance, stability, causality; discrete-time convolution, linear constant-coefficient difference equations, magnitude and phase response. [5 hrs] <u>The Discrete Fourier Transform</u> The discrete Fourier transform (DFT); properties of the DFT; circular convolution; linear convolution via the DFT and the overlap-add method; the radix-2 decimation-in-time fast Fourier transform (FFT) algorithm. [10 hrs] <u>The z-transform and its properties</u> The z-transform, region of convergence for the z-transform, inverse z-transform, z-transform properties. [10 hrs] <u>FIR filter design</u> Generalized linear-phase causal FIR filters; FIR linear-phase filter design using the window method; frequency-sampling design of FIR filters. [10 hrs] <u>IIR filter design</u> IIR filter design using the bilinear transformation; Filter design by impulse invariance response. [10 hrs]

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Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module focuses on fostering active student engagement during exercises, fostering the development of critical thinking skills, and encouraging participation. This will be accomplished through a combination of classroom instruction, interactive tutorials, and the inclusion of engaging experiments that involve sampling activities that capture students' interest. The aim is to refine and enhance students' critical thinking abilities while ensuring their active involvement in the learning process.
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Student Workload (SWL)

الحمل الدراسي للطالب موزع على (15) اسبوع

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	64	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4.26
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Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	125		

Module Evaluation

تقييم المادة الدراسية

		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	5, 10	LO #1-4 , LO #4-9
	Assignments	2	10% (10)	3, 12	LO # 1,2, LO #3-11
	Projects / Lab.	1	10% (10)	Continuous	
	Report	1	10% (10)	13	LO # 1-11

Summative assessment	Midterm Exam	2 hr	10% (10)	6	LO # 1-5
	Final Exam	4hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

	Material Covered
Week 1	Signals, Systems and signal processing Basic element of digital signal processing, Advantages of digital over analog signal processing, Classification of Signals
Week 2	The Concept of frequency in Continuous and Discrete – time signals Continuous – time sinusoidal signals, Discrete – time sinusoidal signals, Harmonically related complex exponential.
Week 3	Analog –to-digital and digital-to-analog conversions Sampling of analog signals, The sampling theorem, Quantization and conversion, Digital-to-analog conversion, Analog-to-digital conversion.
Week 4	Analysis of digital signals and systems.
Week 5	Convolution in discrete time systems
Week 6	Mid-term Exam
Week 7	DE convolution in discrete time systems
Week 8	Discrete-time systems Input/output description of systems, Block diagram representation of discrete-time systems, Classification of discrete-time system, Correlation of discrete-time signals, Properties of correlation.
Week 9	Time domain to frequency domain conversion Discrete-Fourier transform
Week 10	Fast-Fourier transform
Week 11	The Z-transform Direct Z-transform
Week 12	Inverse Z-transform, Properties of the Z-transform.
Week 13	Analogue Filtering versus Digital filtering
Week 14	Design methods of FIR Filters
Week 15	Design Methods of IIR Filters

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

	Material Covered
Week 1	Lab 1: Discrete and Continuous-Time Signals.
Week 2	Lab 2: Discrete-Time Systems.
Week 3	Lab 3: Frequency Analysis.
Week 4	Lab 4: Sampling and Reconstruction.
Week 5	Lab 5: Discrete Fourier Transform.
Week 6	Lab 6: The Z-transform.
Week 7	Lab 7: Digital Filter Design.

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	Digital Signal Processing by John Proakis & D. G. Manolakis, 4/E. Pearson, 2006.	Yes
Recommended Texts	Sanjit K. Mitra, "Digital Signal Processing – A Computer Based Approach", Tata Mc Graw Hill, 2007.	No
Websites	https://www.youtube.com/watch?v=6dFnpz_AEyA&list=PL9567DFCA3A66F299	

Grading Scheme

مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
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MODULE DESCRIPTION FORM

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	Digital Controllers		Module Delivery	
Module Type	Core		☒ Theory ☐ Lecture ☒ Lab ☐ Tutorial ☐ Practical ☐ Seminar	
Module Code	3104CET			
ECTS Credits	5			
SWL (hr/sem)	125			
Module Level	3	Semester of Delivery		5
Administering Department	CET	College	ETC	
Module Leader	Abbas Yousif Awas		e-mail	abbas.yousif@alayen.edu.iq
Module Leader's Acad. Title	Assist Lecturer		Module Leader's Qualification	PH. d
Module Tutor	Dr. Yousif Ward Sakr		e-mail	ywsfward7@gmail.com
Peer Reviewer Name	Dr. Mazen mohammad yousef		e-mail	Mazen.mohammad@alayen.edu.iq
Scientific Committee Approval Date	29/10/2023	Version Number	1.0	

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents

أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية

Module Aims أهداف المادة الدراسية	1. To know the types of microcontrollers and its architecture 2. To understand the difference between the microcontroller and microprocessor 3. dealing with the internal parts of microcontrollers 4. programming the PIC microcontrollers 5. connect the microcontrollers with peripherals to input and output the information 6. Implement interrupts in programs 7. Programming the PIC with the peripherals devices
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	1. Recognize how integrated circuits and microcontrollers works. 2. Known the advantages of using Microcontrollers and Microprocessors. 3. Summarize what is meant by a Peripheral Interface Controller. 4. Describe the PIC Microcontroller. 5. Known type and function of register and SFR in Microcontroller. 6. Explain the A/D (Analog-to-Digital) Converter. 7. Discuss Capture, Compare, and Pulse width modulation modules in PIC microcontrollers. 8. Define and implement interrupts in programs. 9. Explain serial communication systems. 10. Identify how the Oscillator works in an electric circuit. 11. Programming the microcontroller, outputting data/signals, reading data/signals, and character LCD. 12. Application projects of microcontrollers.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following. --Introduction to Introduction to Microcontrollers, Integrated Circuits, General Organization of PIC Microcontrollers: Pins Properties, Registers & Special Function registers, Ports (Input / Output), and Power Supply. Microcontroller Pins Features. The memory unit (ROM, Masked ROM, OTP ROM, UV EPROM, and EEPROM Memory). RAM memory and Flash memory. [15 hrs] --Central Processor Unit (CPU). Interrupt (example of interrupt in a microcontroller). Arithmetical Logical Unit (ALU). Instruction Decoder. Accumulator. Bus (Address Bus and Data Bus). [10 hrs] --Serial Communication, Baud rate, I2C Protocol, SPI (Serial Peripheral Interface), and UART (Universal Asynchronous Receiver/Transmitter) [15 hrs]

	--Oscillator. Timers, using interrupt in timer operating, Watchdog Timer. Counters [10 hrs] --Revision problem classes [5 hrs] --A/D (Analog-to-Digital) Converter, procedure takes place in the A/D converter module, overall plan of ADC, ADRESH, and ADRESL Registers, A/D Acquisition Requirements , ADCON0 Register & ADCON1 Register, Reference Volts. CCP Modules (Capture, Compare, and Pulse width modulation in PIC microcontrollers [19 hrs]
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Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in learning and developing their skills in microcontrollers and logic thinking, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials, and by considering the type of lab experiments involving assignments and project design activities that are interesting to the students.
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Student Workload (SWL)

الحمل الدراسي للطالب موزع على (15) اسبوع

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	64	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4.26
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	61	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	4.06
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	125		

Module Evaluation

تقييم المادة الدراسية

		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	5, 12	LO #1-4, LO #5-10
	Assignments	2	10% (10)	4, 10	LO #1-3, LO #4-9
	Projects / Lab.	1	10% (10)	Continuous	
	Report	1	10% (10)	13	LO # 1-10
Summative assessment	Midterm Exam	2 hr	10% (10)	9	LO # 1-8
	Final Exam	4hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

	Material Covered
Week 1	Introduction to the microcontroller, the difference between MP and Microcontroller
Week 2	The architecture of PIC Microcontroller
Week 3	General Organization of PIC, Registers & Special Function registers
Week 4	Memory Units and CPU
Week 5	I/O ports of the Microcontroller
Week 6	Serial communication, Oscillator, and Timer/Counters
Week 7	Baud rate
Week 8	Programming the Microcontroller
Week 9	Midterm Exam
Week 10	outputting data/signals, Reading data/signals , Character LCD
Week 11	A/D converter & Analog Module
Week 12	On-Chip CCP (Capture, Compare & PWM)
Week 13	Microcontroller Interrupts Programming
Week 14	EEPROM Programming
Week 15	Application projects of Microcontroller

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

	Material Covered
Week 1	Lab 1: Introduction to Micro C with a simple program
Week 2	Lab 2: Counter and Flash LED
Week 3	Lab 3: program using Micro C to count from increasing and decreasing
Week 4	Lab 4: Seven Segment
Week 5	Lab 5: LCD & Switch
Week 6	Lab 6: program using Micro C to input analog signal and read data
Week 7	Lab 7: EEPROM to read and write data.

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	PIC Microcontrollers: An Introduction to Microelectronics, Martin P. Bates. Teach Yourself PIC Microcontrollers, M. Amer Iqbal Qureshi	Yes
Recommended Texts	Interfacing PIC Microcontrollers to Peripheral Devices:2011,	No
Websites		

Grading Scheme

مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX - Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F - Fail	راسب	(0-44)	Considerable amount of work required

Note: Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

MODULE DESCRIPTION FORM

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	Digital Communications		Module Delivery	
Module Type	Core		☒ Theory ☐ Lecture ☒ Lab ☐ Tutorial ☐ Practical ☐ Seminar	
Module Code	3105CET			
ECTS Credits	5			
SWL (hr/sem)	125			
Module Level	3	Semester of Delivery		5
Administering Department	CET	College	ETC	
Module Leader	Dr. Yousif Ward Sakr		e-mail	ywsfward7@gmail.com
Module Leader's Acad. Title	Assist. Professor		Module Leader's Qualification	M.Sc.
Module Tutor	Zahra Abbas Hassan Taiah		e-mail	Zahra.Abbas@alayen.edu.iq
Peer Reviewer Name	Asst. Prof. Ghadir Ibrahim Madi		e-mail	Ghadir.Ibrahim@alayen.edu.iq
Scientific Committee Approval Date	01/10/2025	Version Number	1.0	

Relation with other Modules

العلاقة مع المواد الدراسية الأخرى			
Prerequisite module		Semester	5
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Aims أهداف المادة الدراسية	The aims to students in third stage to defined and understand the - Concepts and terminology used in digital communications - The advantage and disadvantage of each type of digital communication systems - Types of Digital modulation - Send multiple digital signals at the same time and how to retrieve it
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	1- Describe of concepts and terminology used in digital communications 2 -Explain the advantage and disadvantage of each type of digital communications systems 3- Identify types of digital modulation 4- Discuss the comparison between the types of digital systems and its advantages 5- work on digital systems and Describe the most suitable designs 6- Explain how can send more than a signal at the same time and how to retrieve it 7- analog signal into a digital signal converter (PCM) 8- Explain types of digital modulation ask , psk , fsk 9- Explain the modulation and demodulation of quadrature amplitude modulation
Indicative Contents المحتويات الإرشادية	Indicative content includes the following. - Introduction to digital communication & Sampling theorem (10 hr) - Pulse Amplitude Modulation (PAM), Pulse width and Pulse Position

	(10 hr) - Source Coding Techniques Modulation (24 hr) - Baseband modulation (Digital Modulation), (30 hr)
Learning and Teaching Strategies استراتيجيات التعلم والتعليم	
Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.

Student Workload (SWL) الحمل الدراسي للطالب موزع على (15) اسبوع			
Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	64	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعيا	4.26
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	61	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعيا	4.06
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	125		

Module Evaluation تقييم المادة الدراسية					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	4,9	LO #1-4, LO #4- 7
	Assignments	2	10% (10)	3,10	LO #1-4, LO #4- 7
	Projects / Lab.	10	10% (10)	Continuous	LO #1-8
	Report	10	10% (10)	Continuous	LO #1-8
Summative assessment	Midterm Exam	2 hr	10% (20)	6	LO # 1-5
	Final Exam	4hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

	Material Covered
Week 1	Signal types, General block diagram of digital communication
Week 2	Advantage and disadvantage of digital modulation, digital coding
Week 3	Sampling theorem, Pulse Amplitude Modulation (PAM),
Week 4,5	Pulse width and Pulse Position Modulation (PWM & PPM),
Week 6	Time Division Multiplexing (TDM) , Pulse Code Modulation PCM),
Week 7	Mid exam
Week 8	Noise Consideration in PCM, Limitation and Modifications of PCM
Week 9	Differential PCM (DPCM), Delta Modulation (DM),
Week 10	Delta-Sigma Modulation
Week 11	Baseband modulation (Digital Modulation)
Week 12	Amplitude Shift Keying (ASK) [Modulation and demodulation].
Week 13	Frequency Shift Keying (FSK) [Modulation and demodulation],
Week 14	Phase Shift Keying (PSK) [Modulation, Coherent and Noncoherent Detection], Differential PSK.
Week 15	Quadrature Phase Shift Keying (QPSK)

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

	Material Covered
Week 1	Fourier series and Fourier Transform , Spectrum analysis of signal
Week 2	Pulse Amplitude Modulation
Week 3	Pulse Position Modulation (PPM)
Week 4	Pulse Code Modulation
Week 5	Digital Time Division Multiplexing (TDM)
Week 6	Delta Modulation (DM)
Week 7	Amplitude shift key (ASK)

Week 8	Phase Shift Key (PSK)
Week 9	Frequency Shift Key (FSK)

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	- Sarkar N., Elements of Digital Communications, first edition, 2003	NO
Recommended Texts	- Haykin S., Introduction to Analog and Digital Communications, second edition, 2007.	No
Websites	https://www.coursera.org	

Grading Scheme

مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX - Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F - Fail	راسب	(0-44)	Considerable amount of work required

Note: Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54). The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

MODULE DESCRIPTION FORM

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	RealTime Systems		Module Delivery	
Module Type	E		☒ Theory ☐ Lecture ☒ Lab ☐ Tutorial ☐ Practical ☐ Seminar	
Module Code	3106CET			
ECTS Credits	5			
SWL (hr/sem)	125			
Module Level	3	Semester of Delivery		5
Administering Department	CET	College	ETC	
Module Leader	Nadwa Sabeeh Jaber		e-mail	Nadwa.Sabeeh@alayen.edu.iq
Module Leader's Acad. Title	Lecturer	Module Leader's Qualification	PhD	
Module Tutor	Dr. Mazen Youssef		e-mail	Mazen.mohammad@alayen.edu.iq
Peer Reviewer Name	Asst. Prof. Ghadir Ibrahim Madi		e-mail	Ghadir.Ibrahim@alayen.edu.iq
Scientific Committee Approval Date	29/10/2023	Version Number	1.0	

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents

أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية

Module Aims أهداف المادة الدراسية	1. To teach the students about Real-time scheduling and schedulable analysis. 2. To enable the students to Formally specify and verify the timing constraints 3. Design methods for real-time systems 4. Development and implementation of new techniques to advance the state-of-the-art real-time systems research.
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	• correctly and precisely reason about times, events, and action • list and reason about the sources of error and inexactitude in time interval measurement, execution time prediction, and scheduling • empirically estimate the accuracy of a real time clock • measure the execution time of a piece of code • empirically estimate the accuracy and overhead of a real-time scheduler • describe and apply commonly used abstract models and terminology for real-time scheduling and resource management • recognize, classify, and formulate the hard and soft timing requirements of a software system • select an appropriate software architecture and combination of scheduling techniques to satisfy a set of timing requirements • understand and apply the proofs of the fundamental theorems of deadline and fixed priority real-time scheduling • carry out schedulability analysis using deadline and fixed-priority approaches • implement a set of tasks with periodic and aperiodic timing requirements, using C threads and a real-time variant of the Linux operating system • evaluate the suitability of an operating system for real-time applications
Indicative Contents المحتويات الإرشادية	Indicative content includes the following. Part-A [20 Hrs] Introduction to RTS: what is system, what is RT, what is the concept of time in systems, classification, specs of each type, how and when,

	Part-B [20Hrs]
	Scheduling: the concept of scheduling, types, clock, priority, aperiodic, sporadic tasks, resource access, resource control
	Part-C [20 Hrs]
	Multi-processor scheduling: coordination, resource sharing, temporal constraints.
	Part-D [10 hrs]
	RTOS, Datastores, timers, kernels

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module focuses on fostering active student engagement during exercises, fostering the development of critical thinking skills, and encouraging participation. This will be accomplished through a combination of classroom instruction, interactive tutorials, and the inclusion of engaging experiments that involve sampling activities that capture students' interest. The aim is to refine and enhance students' critical thinking abilities while ensuring their active involvement in the learning process.
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Student Workload (SWL)

الحمل الدراسي للطالب موزع على (15) اسبوع

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	64	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4.26
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	61	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	4.26
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	125		

Module Evaluation					
تقييم المادة الدراسية					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	6, 10	LO #1-5, LO #5-9
	Assignments	2	10% (10)	8, 13	LO #1-6, LO #6- 10
	Projects / Lab.	1	10% (10)	Continuous	
	Report	1	10% (10)	14	LO # 1- 12
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-6
	Final Exam	4hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
المنهاج الاسبوعي النظري	
	Material Covered
Week 1	Introduction to RTs
Week 2	Hard Versus Soft Real-Time Systems
Week 3	A Reference Model of Real-Time Systems
Week 4	Commonly Used Approaches to Hard Real-Time Scheduling
Week 5	Clock-Driven Scheduling
Week 6	Priority-Driven Scheduling of Periodic Tasks
Week 7	Midterm Exam
Week 8	Scheduling Aperiodic and Sporadic Jobs in Priority-Driven Systems. Resources and Resource Access Control
Week 9	Clock sync, timers, Kernels
Week 10	RT in distributed Systems
Week 11	Scheduling in multi-processors
Week 12	Clock Sync.
Week 13	Hardware, timers, Kernels
Week 14	RTOS
Week 15	Real Time data stores

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

	Material Covered
Week 1	Arduino UC
Week 2	Static loops
Week 3	Dynamic loops
Week 4	Watchdog
Week 5	Timers
Week 6	Arduino RTOS
Week 7	
Week 8	
Week 9	
Week 10	
Week 11	Network app (client)
Week 12	Network app (server)
Week 13	Network app (UDP)
Week 14 & 15	Proto-typing

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	Real-Time Systems, Jane W. S. Liu, 2000	NO
Supporting Texts		No
Online resource	https://www.youtube.com/watch?v=yShUSwskUNA&list=PL1iLu2CSC9EU4mMByEhBp9CcYgAliDs_v https://personal.utdallas.edu/~cxl137330/courses/fall13/RTS/RTS.html http://www.cs.fsu.edu/~baker/realtime/syllabus.html#Objectives	

Grading Scheme مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX - Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F - Fail	راسب	(0-44)	Considerable amount of work required
Note: Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				